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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Examiner:

CHANG, DANIAL D

MADURAWE

Art Unit:

2819

Application No.: 10/751,324

Filed: 01/05/2004

For: Integrated Circuits with RAM and

ROM fabrication options

AMENDMENT AND RESPONSE

04-17-2006

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

Sir:

In the Office Action dated 04/05/2006, the examiner noted the following:

- (1) Claims 1 & 17 are objected because of lack of clear distinction between preamble & body of the claim.
- (2) Claims 1-20 are rejected under 35 USC 102(b) as being anticipated by Duong et al (US 5,600,264).

In response, the Applicant has modified Claims 1 & 17. The applicant has further provided arguments to show that the rejected claims are not anticipated by Duong. A list of all claims is provided. Withdrawal of the objections and rejections is respectfully requested.

Please amend the claims as listed below.

(This document was faxed to Commissioner for Patents on 04/17/2006)

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